

FIG. 1

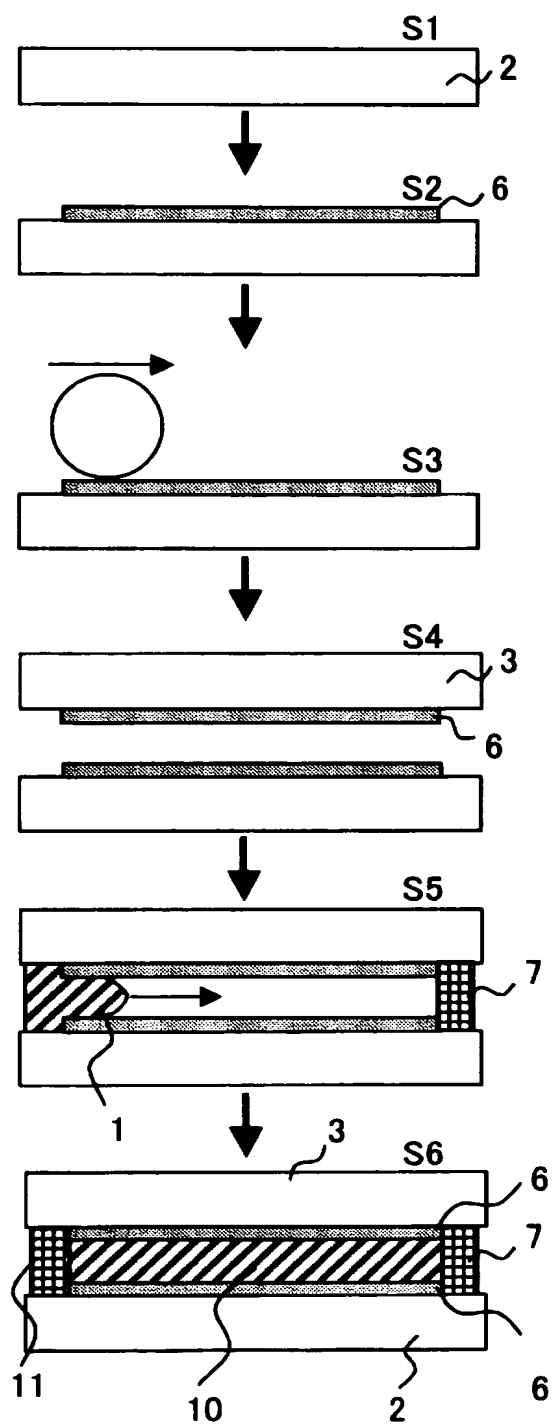
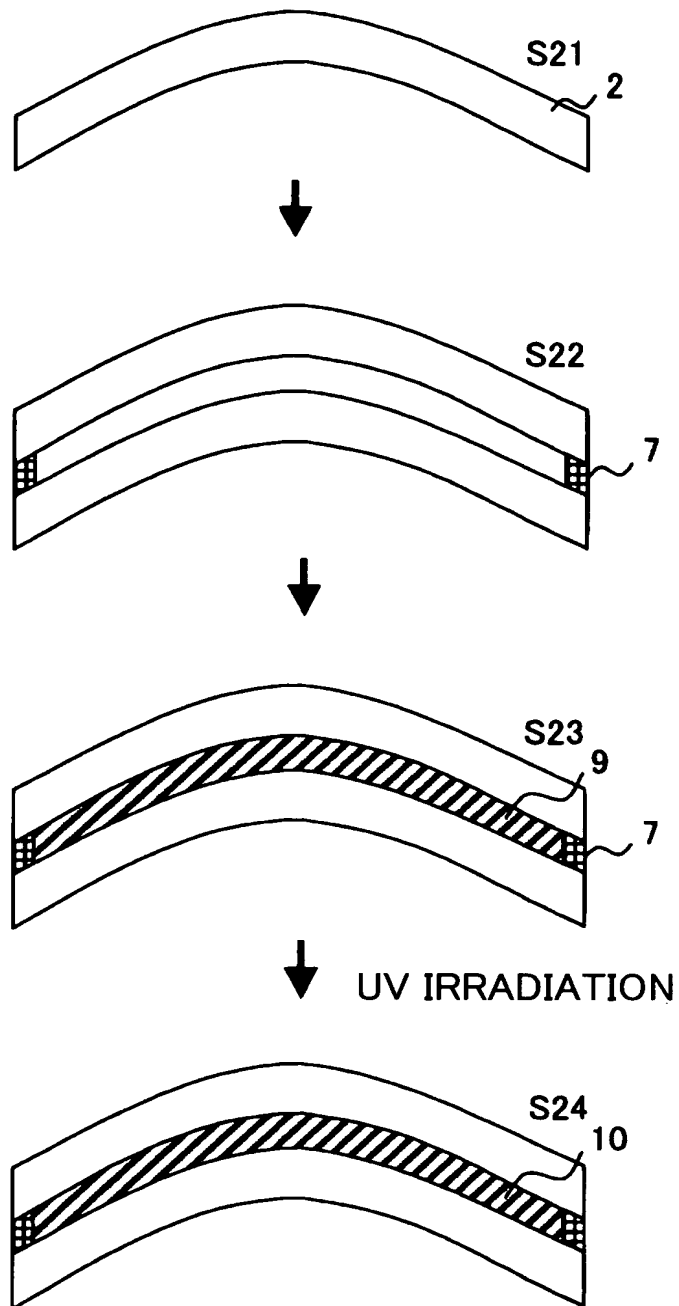
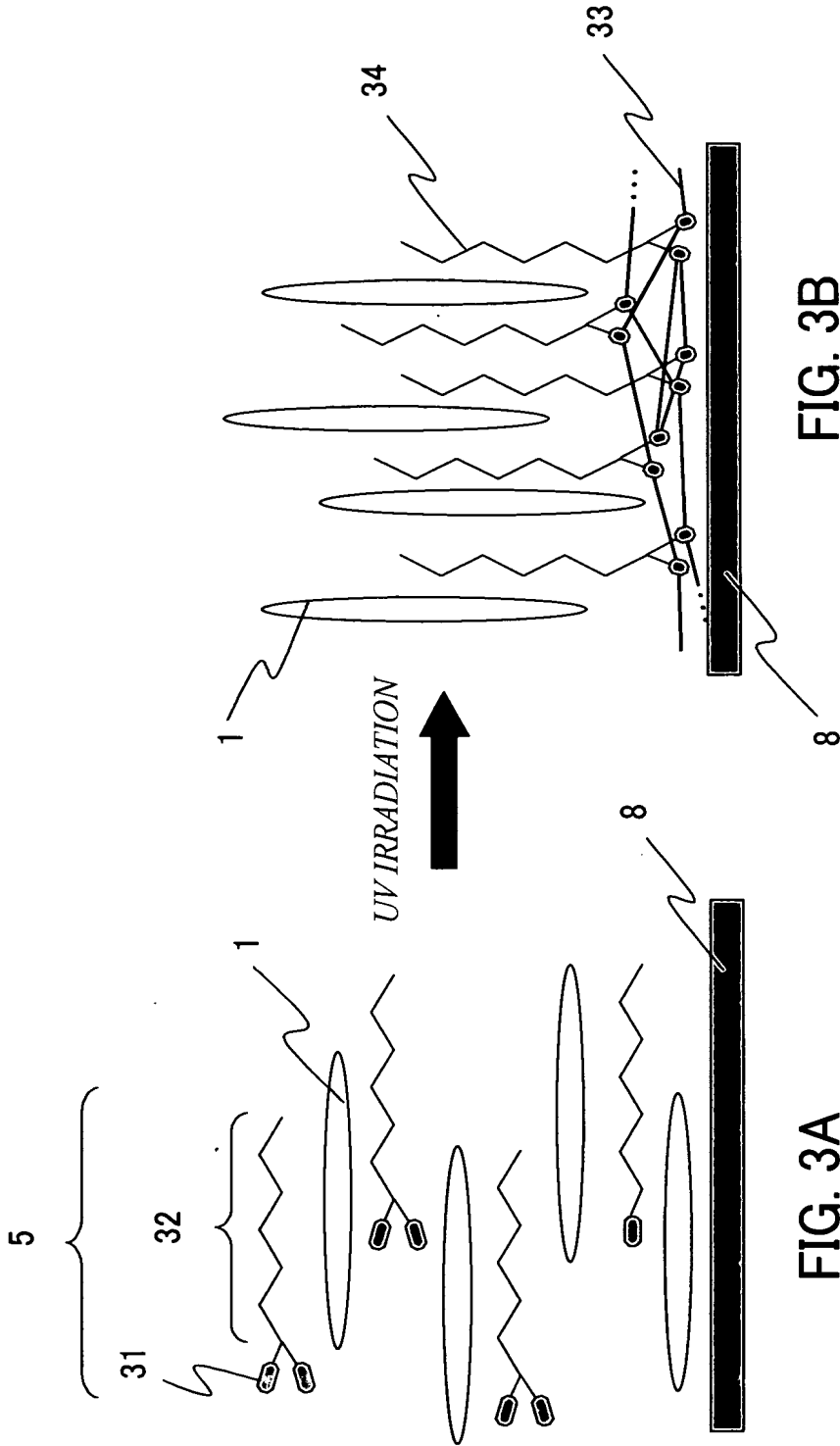


FIG. 2





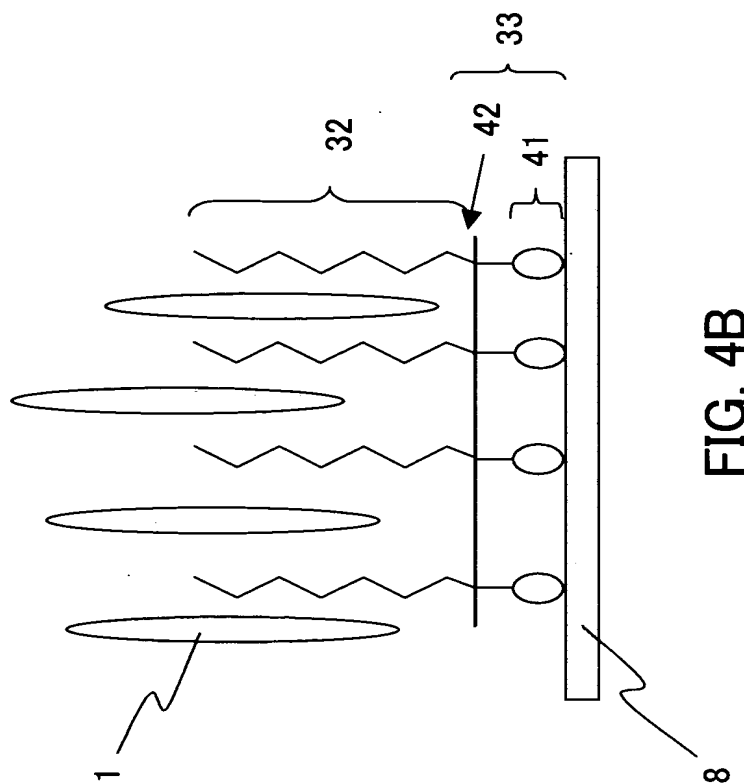


FIG. 4B

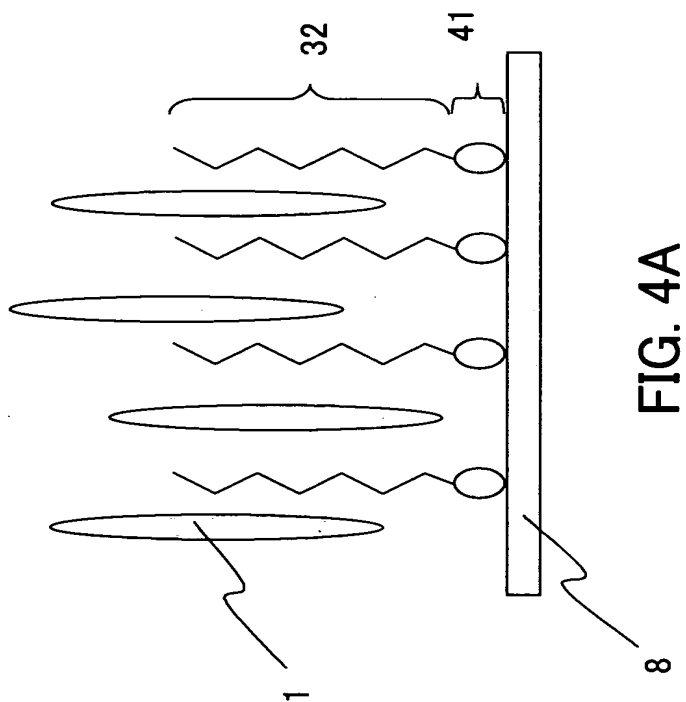


FIG. 4A

FIG. 5

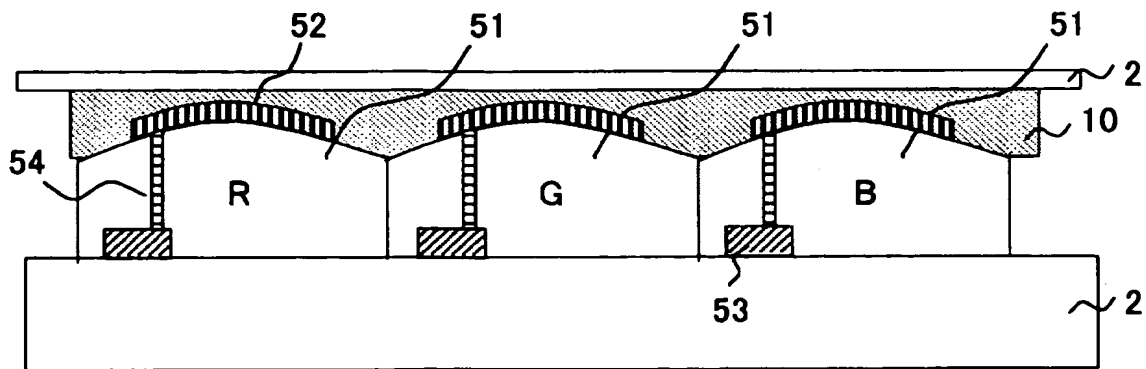


FIG. 6

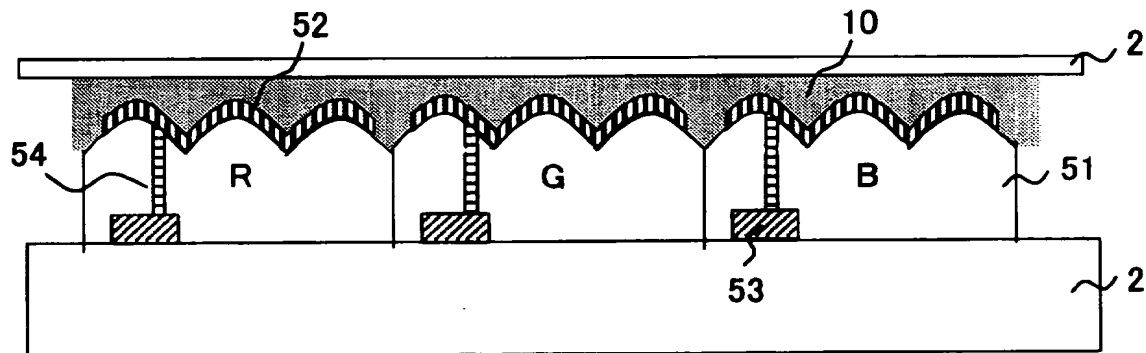


FIG. 7

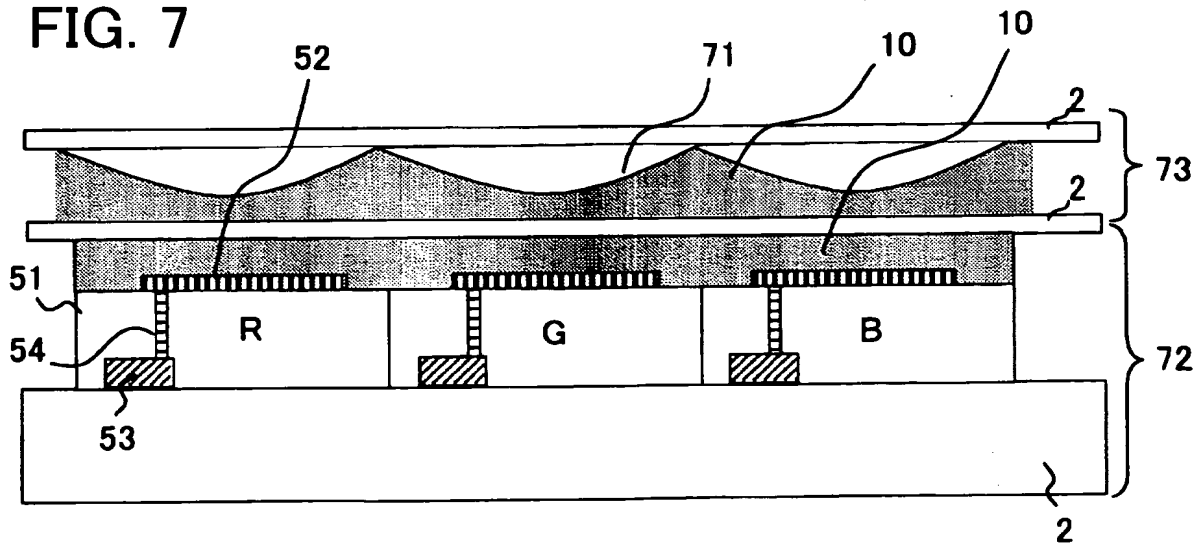


Fig. 8 is a perspective view of the device 10. It shows a rectangular block with a top surface 2 and a side surface 2.

This cross-sectional view shows a color filter assembly. A substrate 10 is shown with a top layer 2. Below the top layer, there are three color regions labeled R (Red), G (Green), and B (Blue). Each color region is separated by a vertical partition 51. A common electrode 52 is formed on the top surface of the color regions. A pixel electrode 54 is formed on the top surface of each color region. A pixel opening 53 is formed in the top layer 2, exposing the pixel electrode 54. The pixel opening 53 is defined by a vertical partition 51. The entire assembly is mounted on a base 2.